

# Overview

①

Programming Languages (PL)



④

Program Analysis (PA)



③

Optimization



②

Runtime Environments

# PL Categories

lambda calculus (function definition, application, recursion)

ML  
Haskell  
LISP  
...

Functional  
Programming  
Languages

- correctness! (efficiency?) instead of loops!
- no state (no assignment)
- no side effects
- good for parallelization

stack

heap (for closures)

↳ garbage collection

declarative

logic formulae (eg. Horn logic)

- not just procedural →
- correctness!
  - logical interpretation
  - proof search = execution

Logic  
Programming  
Languages

Prolog

polymorphism (types, procedures)

theorem prover  
resolution  
unification

Object-Oriented  
Programming  
Languages

- correctness!
- abstraction
- encapsulation

inheritance  
dynamic binding

C++  
Java  
Smalltalk  
...

concurrent, parallel, distributed

Concurrent  
Programming  
Languages

Go  
Occam  
...

- correctness!
- shared memory
- message passing

communication } GC helps!  
coordination

# Dynamic Heap Management

slow vs. fast, external fragmentation vs. internal fragmentation  
 partitioning  
 random deallocation order

allocate, deallocate contiguous memory chunks

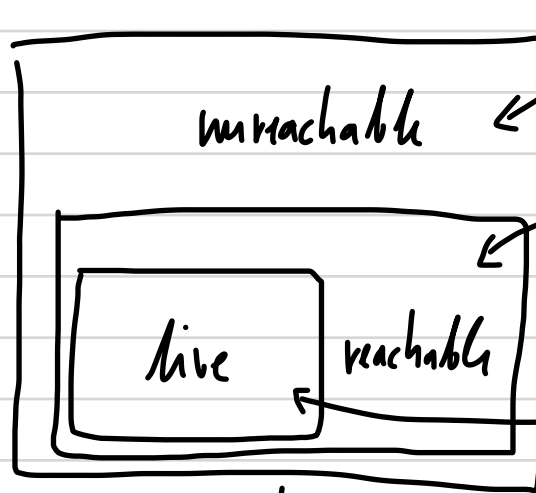
Stack  
 Buddy  
 Doug Lea  
 TLSF  
 Compact-fit  
 Short-term Memory  
 ...

Memory Allocators

coalescing  
 compaction

- model memory depends on program input
- malloc, free
- dangling pointers
- memory leaks

GC approximates object liveness through reachability (or unreachability)



via roots:  
 • globals  
 • locals, params  
 • stack

Garbage Collectors

Mark-Sweep  
 Semi-Space  
 Generational (Nursery, Mature Space)  
 ...  
 Metronome  
 ...

tracing  
 reference counting

- safety (dangling pointers)
- memory leaks
- $O(\text{heap size})$
- reachable memory leaks still possible
- latency

concurrent, incremental → real-time  
 stop the world!

# Optimization Classes

	compiler	output
time	compilation performance	execution performance
space	code size data size	code size data size

→ no global optimum because goals are contradictory

Optimization classes:

1. modification of source code (not considered here)
2. modification of generated code:

- target-independent

- target-dependent

- "context-free"

} possible with  
single-pass  
compiler

- "context-sensitive" (requires internal representation of source code)

↳ multi-pass compiler

# Single Pass

## 1. target-independent:

$$x + 0 == x$$

$$x * 2 == x + x == x \ll 1$$

$$b \&\& \text{true} == b \mid \mid \text{false} == b$$

$$b \mid \mid !b == \text{true}$$

...

## 2. target-dependent:

- RISC vs. CISC (instructions combining multiple
- ARM vs. Intel RISC instructions)

## 3. "context-free":

- constant folding
- multiplication by a power of 2 can be replaced by a left shift operation:

$$x * 2^k == x \ll k$$

- division by a power of 2 can be replaced by a right shift operation:

$$x / 2^k == x \gg k$$

...

# Context

## 1. elimination of common subexpressions:

```
x = (a + b) / c;  
y = (a + b) / d;  
    ↙  
u = a + b;  
x = u / c;  
y = u / d;
```

→ optimizes # of arithmetic operations but not # of assignments

## 2. array addressing and hidden common subexpressions:

$a[i][j] = a[i][j] + b[i][j];$

→ same multiplication done three times ( $i \cdot \text{size}(\text{array})$ ,  $j \cdot \text{size}(\text{element})$ )  
→ same address computation for contiguously allocated arrays  
( $i \cdot \text{size}(\text{array}) + j \cdot \text{size}(\text{element})$ )

## 3. loop invariants:

→ code motion

```
while (i < 10) {  
    z = x + y;  
    i = i + 1;  
}
```

```
while (i < 10) {  
    a[i] = b[i] + c[i];  
    i = i + 1;  
}
```

## 4. recurrence relations:

$\text{address}(a[i]) = \text{address}(a) + i \cdot \text{size}(\text{element})$

→  $\text{address}(a[i+1]) = \text{address}(a[i]) + \text{size}(\text{element})$

## 5. register allocation:

→ using registers only for intermediate results is wasteful

→ use registers also for variables: graph coloring  
(too few registers result in spilling)

# PA and Optimization

[Modern Compiler Implementation  
in Java, by A. Appel]

→ construct  
control-flow  
graph (CFG)  
first!

common subexp.  
dead code  
uninitialized vars.  
...

Liveness  
Analysis

← will a variable be  
used in the future?

Dataflow  
Analysis

least fixed  
point of  
data-flow  
equations

Register  
Allocation

• dominators  
(to find loops in CFG)  
• loop invariants  
(constants, out-of-loop  
operands are loop-  
invariant)

Loop  
Optimizations

loop unrolling

induction variables  
 $\begin{cases} j = i \cdot c + d, i = i + a \\ \rightarrow j = j + c \cdot a \text{ if } \\ c, d \text{ loop-invariant} \end{cases}$

variables not live  
at the same time  
may be held in  
the same register

graph  
coloring

Static  
Single-Assignment  
Form

SSA -

each variable is  
only assigned  
once (statically)  
→ simplifies  
data-flow  
analysis!

Pipelining,  
Scheduling

↑ parallelism  
↓ CPU

Memory  
Hierarchy

↑ cache-alignment  
pgm-alignment  
→ hardware  
→ OS

That's it!  
Thanks a lot!